

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
)	
Hiroshi MAKINO)	Group Art Unit: 2823 (expected)
)	
Application No.: Unassigned)	Examiner: G.R. Fourson, III
)	(expected)
Filed: August 4, 2003)	
)	Confirmation No.: Unassigned
For: SEMICONDUCTOR INTEGRATED)	
CIRCUIT DEVICE)	
)	

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: August 4, 2003

Sir:

This Preliminary Amendment is filed in order to facilitate processing in the above-identified application. Please amend the above-identified application as follows: